

K. D. K. COLLEGE OF ENGINEERING, NAGPUR
DEPARTMENT OF INFORMATION TECHNOLOGY

B. E. THIRD SEMESTER

BEIT304P: DIGI TAL ELECTRONICS AND FMP

LIST OF EXPERIMENTS

1. TO VERIFY THE BASIC LOGIC GATES USING DIODES AND TRANSISTORS.
2. TO CONSTRUCT HALF ADDER, HALF SUBTRACTOR, FULL ADDER, FULL SUBTRACTOR USING AND, OR, NOT AND EX-OR GATES AND VERIFY THE TRUTH TABLE.
3. TO VERIFY BOOLEAN EXPRESSION USING LOGIC GATES.
4. TO STUDY AND VERIFY DEMORGAN'S THEOREM.
5. IMPLEMENTATION OF EX-OR AND EX-NOR GATE USING NAND GATE ONLY.
6. TO STUDY R-S & CLOCKED R-S FLIP-FLOP, D & CLOCKED D FLIP FLOP.
7. TO STUDY J-K FLIP FLOP
8. TO VERIFY THE PRINCIPLE OF MOD-N COUNTER.
9. TO STUDY ARCHITECTURE OF 8085.
10. WRITE AN ASSEMBLY LANGUAGE PROGRAM TO MULTIPLY TWO 8 BIT DATA.