K.D.K. COLLEGE OF ENGINEERING NANDANVAN, NAGPUR-24

DEPARTMENT OF ELECTRONICS ENGINEERING

SESSION 2018-19

Sub: - Embedded Systems | Date: 18/07/2018 | Sem.:- VII Sem.

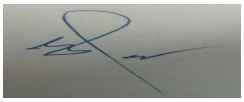
Assignment No-I

Bloom's Taxonomy Levels – 1. Remember 2. Understand 3. Apply 4. Analyze 5. Evaluate 6. Create

Question no 1, 2, 3 are based on CO702.1 - Identify the Technical & financial issues related to Embedded System design
Question no 4, 5, 6 are based on CO702.2 Select various types of Processors & Peripherals required to design an embedded processor

Que. No	Question	BTL level
Q.1 (a)	What is embedded system & explain its characteristics	L2
(b)	List the applications of embedded system according to domain.	L2
Q.2(a)	Give comparison between RISC & CISC.	L2
(b)	Explain the design process of embedded systems	L2
Q.3	What are the design challenges in embedded system? Explain in details	L2
Q.4	Explain the terms NRE cost, Unit cost, per product cost, total cost using suitable example.	L3
Q.5 (a)	Explain the memory architectures of embedded systems	L2
(b)	What are the attributes of embedded systems	L2
Q.6 (a)	Explain processor selection criteria	L2
(b)	Draw & explain the hardware architecture of embedded systems	L3

Last date for submission is 26/07/2018



S. A. Bagal
Subject Teacher

K.D.K. COLLEGE OF ENGINEERING NANDANVAN, NAGPUR-24

DEPARTMENT OF ELECTRONICS ENGINEERING

SESSION 2018-19

Sub: - Embedded Systems | Date: - 24/08/2018 | Sem.:- VII Sem.

Assignment No-II

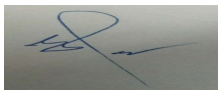
Bloom's Taxonomy Levels – 1. Remember 2. Understand 3. Apply 4. Analyze 5. Evaluate 6. Create

Question no 1, 2, 3 are based on CO702.3 - Describe Architecture, modes of operations, Exceptions and write assembly language program of ARM Processors.

Question no 4, 5, 6 are based on CO702.4 - Describe various communication Protocols used in embedded system

Que. No	Question	BTL level
Q.1	Draw & Explain the functional block diagram of core of ARM7 processor.	L3
Q.2	Write Short Note on "Defining the constants" in ARM environment.	L2
Q.3	Compare LDR & LDM Instructions with respect to its operation.	L2
Q.4	Draw & explain I2C Protocol. Also give its salient features	L3
Q.5	Write short Notes on CAN Protocol	L2
Q.6	Discuss Dead-lock & how it can be avoided	L2

Last date for submission is 31/08/2018



S. A. Bagal
Subject Teacher

K.D.K. COLLEGE OF ENGINEERING NANDANVAN, NAGPUR-24

DEPARTMENT OF ELECTRONICS ENGINEERING

SESSION 2018-19

Sub: - Embedded Systems Date: - 12/09/2018 Sem.:- VII Sem.

Assignment No-III

Bloom's Taxonomy Levels – 1. Remember 2. Understand 3. Apply 4. Analyze 5. Evaluate 6. Create

Question no 1, 2, 3 are based on CO702.5 - Explain Real Time Operating System Concepts.
Question no 4, 5, 6 are based on CO702.6 Prepare the case studies based on different embedded domain.

Que. No	Question	BTL level
Q.1	Draw & Explain the functional block diagram of core of ARM7 processor.	L3
Q.2	Write Short Note on "Defining the constants" in ARM environment.	L2
Q.3	Compare LDR & LDM Instructions with respect to its operation.	L2
Q.4	Draw & explain I2C Protocol. Also give its salient features	L3
Q.5	Write short Notes on CAN Protocol	L2
Q.6	Discuss Dead-lock & how it can be avoided	L2

Last date for submission is 18/09/2018

S. A. Bagal
Subject Teacher