

K.D.K. COLLEGE OF ENGINEERING

Department of Electronics Engineering

B. E. Fifth Semester Electronics Engineering

Subject : Analog Circuits and Design

Subject Code : BEENE503P

Duration : 2 Hr.

College Assessment : 25 Marks

University Assessment : 25 Marks

List of Experiments:

1. To study the inverting and Non-Inverting operational amplifier
2. To study OP-AMP as an Adder and Subtract or
3. To plot the frequency response of Non-Inverting operational amplifier
4. To study Integrator and Differentiator circuits using operational amplifier
5. To study the Schmitt Trigger circuit using operational amplifier
6. To study the precision Half wave and Full wave Rectifier
7. To design Wein Bridge Oscillator circuit using operational amplifier
8. To design Phase Shift Oscillator circuit using operational amplifier
9. To design Emitter follower voltage regulator Circuits
10. To design 1st order Low pass Filter circuit using operational amplifier
11. To design 1st order High pass Filter circuit using operational amplifier
12. To design Series Voltage regulator Circuits
13. To design IC 723 as voltage regulator

Signature of the Lab Incharge

Signature of the Subject Teacher